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A multi-objective heuristic method for optimal design of HTS fault current limiters

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KEYWORDS

Multi-objective decision making; Multi-objective simulated annealing; High temperature superconductors; Superconducting fault current limiter. **Abstract.** The optimization algorithm for High-Temperature Superconducting (HTS) Fault Current Limiters (FCL) normally includes some non-commensurable criteria or objectives such as cost, limiting factor, thermal stresses, and mechanical stresses that should be minimized or maximized concurrently in a protracted optimization process. It can be performed only by tools which firstly describe the HTSFCL precisely and predict its limitation behavior and then select the optimum design using optimization algorithm. Multi-Objective Decision Making (MODM) is one of the most widely used decision techniques in the business and engineering worlds. In the MODM problems, there are several objectives of the system concurrently optimized, and a solution set, i.e. the Pareto front, is usually obtained instead of a real optimal solution. This paper explains in details the combination models of HTSFCL as a component in PSCAD/EMTDC and presents the optimization algorithm based on a new approach of normalized multi-objective simulated annealing.

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1. Introduction

The High-Temperature Superconducting (HTS) Fault Current Limiter (FCL) is one of the main solutions proposed to reduce high fault currents in rapidly growing electrical systems. The advantages of HTSFCL, such as automatic fault current sensing, automatic recovering, and faster limiting operations, are expected to be the unique countermeasure to solve the drawbacks of the other limiting devices.

The superconducting state is defined by three important factors: critical temperature (T_c) , critical magnetic field (H_c) , and critical current density (J_c) . Each of these parameters is very dependent on the other two properties present. The limiting operation is a multilateral interaction between the fault current, temperature, magnetic field, and current dependent impedance in addition to other specifications in the external power system.

Hence, for an optimal design of parameters and dimensions, a comprehensive model describing HTS-FCL behavior for different statuses of power system is needed [1]. The optimization algorithm for HTSFCL generally includes some non-commensurable criteria or objectives such as cost, limiting factor, and thermal and mechanical stresses being minimized or maximized simultaneously in a protracted process. This can be accomplished only by tools being capable of describing the HTSFCL and precisely predicting its limitation behavior followed by selecting an optimum design via optimization algorithm.

Hence comes the need for an advanced technique for optimization concepts based on physical and elec-

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trical properties of the HTSFCL. However, as far as the authors are aware, such software does not exist among commercial tools.

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Various types of HTSFCL devices exist, including normal resistive, flux flow resistive, magnetic shield inductive, transformer inductive, DC reactor, fluxlock, and saturated core [2-4]. Among these FCLs, inductive-type superconducting fault current limiters (LSFCL) have made themselves prominent because of large design flexibility due to turn ratio and multi benefits of not needing current lead causing isolation between FCL and power transmission line, heat loss reduction (lower resistance), and smaller impedance due to inductive impedance of power system [5,6].

However, the optimization process can readily be used it in any types other than that described above, without departing from the spirit of the concept.

The LSFCL generally consists of a primary copper coil and a secondary superconductor cylinder/coil wound around a closed or open magnetic iron core. In the shielded core LSFCL, superconducting cylinder is fixed between copper coil and magnetic core; therefore, screen currents cause no flux penetration into iron core in normal condition. In the transformer type, secondary winding is a cooper coil shortened via a superconducting component, resulting in nearly zero impedance from primary side. In the event of a fault, a superconducting to normal (S/N) transition occurred in both LSFCLs and reflected limiting impedance appeared in the primary side [5].

For non-consistent objectives, multi-objective optimization algorithms attempt to find proper solutions in Multi-Objective Decision Making (MODM) problems. In general, such problems have multiple solutions that constitute the Pareto optimal set instead of a real optimal solution. Considering all objectives, these solutions are optimal in a sense that no other solutions in the search space are superior to one another. One of the most frequent methods in obtaining Pareto solutions has been realized by the application of heuristic methodologies, such as Genetic Algorithms (GA), Evolutionary Algorithms (EA), and Simulated Annealing (SA). These methods are based on the phenomena of their principles being observed in nature, from which SA is a robust algorithm for solving single-objective optimization problems [7]. The major advantage of SA over the other methods is the uniqueness of its non-global trapped local minima. The algorithm employs a random search which accepts not only the changes that decrease objective function, but also some changes that increase it. D. Geman and S. Geman [8] proposed that the designed algorithm converges to a global optimum when the annealing process is accomplished sufficiently slowly. A number of comparisons of the multi-objective SA algorithms have already been reported [9]. SA optimization algorithms

in single- or multiple-objective forms have also been used for designing some superconducting devices such as microwave filter [10], magnet [11], energy storage device [12], generator [13], and resistive fault current limiter [14,15] based on their capability to find the global optimum.

This paper attempts to supplement our previous report on the normalized prioritized multi-objective simulated annealing [14] by presenting a new optimization algorithm being improved by a new normalization method. This was accomplished by performing numerical analysis of electrical and thermal behavior of the LSFCL in a feasible PSCAD/EMTDC model so as to select the optimized design via the proposed algorithm.

2. Inductive-type superconducting fault current limiter

As far as limiting impedance is concerned, HTSFCLs have been classified into resistive type (RSFCL), inductive type (LSFCL), and hybrid type (HSFCL) [16]. However, some of the previous reports have divided the LSFCLs into quench and non-quench types. The former, in which S/N transition occurs, includes the magnetic shielding type, the transformer type, and the ring type; while the latter includes the saturation reactor and dc reactor types. Except negligible difference during normal system operation, LSFCLs all act similarly in fault condition. Therefore, a common model can be considered for the current limiting regime.

In the quench-type LSFCL, generally, the primary copper coil and a secondary superconductor tube/coil were wound around a closed/open magnetic iron core. In the shielded core LSFCL, superconducting tube (shown in Figure 1) is fixed between copper coil and magnetic core; thereby, the screen currents would prevent flux penetration into iron core during normal operation. Another version of LSFCL is the transformer type with copper secondary winding shortened by an



Figure 1. Magnetic shield type superconductive fault current limiter.



Figure 2. (a) Common model of LSFCL. (b) Corresponded equivalent circuit.

HTS element "resistor $(R_{\rm SFCL})$ " placed in cryostat and cooled down by liquid nitrogen via a current lead, giving almost zero impedance on the primary side. Figure 2 shows the corresponding common equivalent circuit for the aforementioned LSFCLs in which R_2 does only exist for considering transformer (secondary copper winding). In this figure, i_p and i_s are the primary and corresponding secondary currents, respectively. Although the superconductor materials used in the transformer type are fewer, there is an effective magnetic coupling that causes more core loss and secondary copper loss than magnetic shielded type [2].

In the event of a fault occurring, the increasing current exceeds the critical value of the HTS component; then, the resistance of the secondary winding is reflected into the primary circuit; and the magnetic flux penetrates the iron core, increasing the impedance of limiter [5,17].

Considering Figures 1 and 2, under fault conditions, the high current in the copper coil exceeds the shielding capability of the superconductor tube, leading to a jump in impedance that is flux penetration into the iron core. As a result, the limiting impedance is formed by the magnetizing inductance of the transformer in parallel to the resistance of the superconducting tube, reflected on the primary side by the factor of $(N_1/N_2)^2$. The design parameters mainly determine the resistive or inductive characteristic of the limiting impedance.

The advantage of LSFCLs is that the secondary winding of the magnetic shield and transformer types can be made of either a single or few HTS rings. The working principle of this type of limiter is based on the field screening effect of the HTS, which drives the magnetic core to a zero flux condition in superconducting state [18]. This type of LSFCL is similar to the transformer type with no secondary copper winding. Therefore, a common model (Figure 2) can be considered based on the aforementioned behavior in which electrical equivalent circuit of transformer is accompanied with a variable load [19].

Different types of LSFCLs have some advantages including physical separation of electric circuit and superconducting material, lower voltage drop across the superconducting element during fault conditions, and possibility of making the superconducting coil in simple BSCCO rings or cylinders [20]. Likewise, the secondary coil can be made from the new generation of economical superconducting wires named YBCO coated conductors [21].

In these figures, R_1 , R_2 , L_1 , L_2 , M, L_s , and L_m are the resistance of primary and secondary windings (for magnetic shield type $R_2 = 0$), the self-inductance of primary and secondary windings, the mutual inductance between primary and secondary windings, the stray inductance of primary winding, and the magnetizing inductance of transformer, respectively. The turn ratio of transformer is denoted by $n = (N_1/N_2)$.

By considering h, r_c , r_p , and r_s as the height of the windings, core radius, primary radius, and radius of secondary coil/tube, respectively, the inductances can be calculated as [19]:

$$L_1 = \frac{\pi\mu_0 n^2}{h} \left\{ r_p^2 + (\mu_r - 1)r_c^2 \right\},$$
(1)

$$L_2 = \frac{\pi\mu_0}{h} \left\{ r_s^2 + (\mu_r - 1)r_c^2 \right\} = \frac{M}{n},$$
(2)

$$M = \frac{\pi\mu_0 n}{h} \left\{ r_s^2 + (\mu_r - 1)r_c^2 \right\},$$
(3)

where, μ_r is the relative permeability of the core. If the transformer model of LSFCL is simplified by Figure 2(b), the stray and magnetizing inductances can be written as:

$$L_s = (1-k)L_1 = L_1 - n^2 L_2 = \frac{\pi\mu_0 n^2}{h} (r_p^2 - r_s^2), \quad (4)$$

$$L_m = kL_1 = nM = \frac{\pi\mu_0 n^2}{h} \left\{ r_s^2 + (\mu_r - 1)r_c^2 \right\}.$$
 (5)

The change of the resistance of HTS can be estimated by a non-linear voltage-current characteristic, including three portions corresponding to the flux creep, flux flow, and normal state described in [22]. Moreover, the total power loss of HTS is the sum of Joule loss and the ac loss:

$$P_{sc} = P_j + P_{ac}.$$
 (6)

Thus, generating heat and HTS temperature can be calculated by integration of power loss [14]:

$$T = \frac{1}{C_p} \left(\int P_{sc} dt - \frac{P_{cs} \Delta t}{V_{sc}} \right) + T_0, \tag{7}$$

where, P_{cs} is the cooling system power, and V_{sc} , Δ_t , and C_p are the volume of the HTS coil, fault duration time, and specific heat of HTS material and stabilizer. In general, stabilizer of HTS, which is made of metals (i.e., silver, steel, copper, etc.), acts as a shunt resistance bypassing the current and heat during quench time. The stabilizer helps to avoid destructive hot spots caused by local quench [23].

To calculate the ac loss, Bean model [24] (used by [19]) or Norris model [25] can be used as:

$$P_{ac} = \frac{8\sqrt{2}}{3} f \mu_0 \frac{r_s}{h^2} \frac{(ni_p)^3}{j_c},\tag{8}$$

$$P_{ac} = \frac{f\mu_0}{\pi} \frac{i_s}{j_c} \left[\left(1 - \frac{i_s}{i_c} \right) \ln \left(1 - \frac{i_s}{i_c} \right) + \left(2 - \frac{i_s}{i_c} \right) \frac{i_s}{2i_c} \right],\tag{9}$$

where, f, i_p , i_s , i_c , and j_c are frequency, primary current, secondary current, critical current, and critical current density of HTS, respectively. In addition to ac loss, the core losses and current lead loss must also be taken into account for transformer type. Since the secondary side is short-circuited by a superconducting element, the core loss is very low, that is 0.01% - 0.1%of transformer rating [20]. Furthermore, the minimum loss due to current lead (if exists) is estimated by Eq. (10) assuming temperature-dependent coefficients κ and ρ as average values of thermal conductivity and resistivity, respectively [26]:

$$P_{cl} = 2ni_p \sqrt{\rho \kappa \Delta T}.$$
(10)

3. Multi-objective optimization method

Generally, engineering design problems require a simultaneous optimization for several incompatible objectives. To study the trade-offs that exist between these conflicting design objectives/goals and to explore design options, one needs to formulate the optimization problem with multiple-objective decision making techniques. These optimization methods seek some optimum designs that attain the multiple objectives as closely as possible while strictly satisfying constraints [27].

In a multi-objective optimization problem consisting of N_v variables, $N_f \geq 2$ objective functions are optimized subject to N_c constraints:

$$\min \{f_i(x_j)\} \ \forall \ i = 1, 2, ..., N_f \ \text{and} \ j = 1, 2, ..., N_v$$
(11)

Subject to:

$$g_k(x_j) \le 0$$
 $k = 1, 2, ..., N_c$

A point $X' = \{x'_1, x'_2, ..., x'_{N_v}\}$ is defined as being

Pareto-optimal if and only if there is no other point, $X = \{x_1, x_2, ..., x_{N_v}\}$, which can dominate it:

$$\begin{cases} \forall i = 1, 2, ..., N_f \quad f_i(X) \le f_i(X') \\ \forall i \qquad \qquad f_i(X) < f_i(X') \end{cases}$$
(12)

One of the most useful techniques for solving complicated optimization problems, the simulated annealing, was introduced by Kirkpartick et al. [7]. This technique was originally inspired by the formation of crystals in solids during cooling. The method itself has a direct similarity with thermodynamics, specifically with the way that liquids freeze and crystallize, or metals cool and anneal. In order to simulate the annealing process of metals, the material is considered as a system of particles. The probability of a particle being in a specific energy level (ΔE) is expressed by the Boltzmann probability distribution:

$$P(\Delta E) = \exp\left(-\frac{\Delta E}{KT}\right),\tag{13}$$

where, K and T are the Boltzmann constant and temperature, respectively. Kirkpatrick created a variation of the classical local search method with an important difference. According to some probability function based on Boltzmann probability distribution, the inferior moves are accepted. Starting the algorithm, almost all the inferior moves are accepted so that the procedure operates as a random search. However, as the temperature decreases, fewer inferior moves are accepted; but the algorithm still has the ability to escape from local optima [28].

In the recent years, several SA-based algorithms, e.g. Weight-Based Multi-Objective Simulated Annealing (WMOSA) [29], Pareto dominant-based multi-objective simulated annealing (PDMOSA) [30], Archived Multi-Objective Simulated Annealing (AMO-SA) [31], Prioritized Multi-Objective Simulated Annealing (PMOSA) [28], and Dominance Cost variant Multi-Objective Simulated Annealing (DCMOSA) [32], have been presented for optimization of engineering problems.

In PMOSA, the algorithm satisfies the given priority of objectives by assigning a different initial temperature for each objective. Furthermore, contentment of the constraints is considered as a top priority being incorporated as additional terms in the first objective function:

$$F_1(X) = f_1(X) + b \sum_{k=1}^{N_c} \operatorname{Max} \left\{ 0, g_k(X) \right\},$$
(14)

where b is used to change the relative importance of additional terms and N_c is number of constraints. Regarding the importance priority of objective functions,



Figure 3. PMOSA optimization algorithm.

 N_f temperatures are used to obtain Pareto front. The steps of the PMOSA algorithm can be summarized as shown in Figure 3.

In this algorithm, if the variables and objective functions are normalized to compare and combine different types of parameters, the normalized PMOSA (henceforth, NPMOSA) is obtained. This improvement is very important; because in optimizing design of an HTSFCL, many parameters and functions have not the same dimensions and magnitude, since the probabilities of acceptance are influenced by the values of the objective functions. Thus, using only various initial temperatures for objective functions cannot be feasible.

In NPMOSA algorithm, differential of probabilities (ΔF_i) for any function is normalized by being divided to the present value. Consequently, they are comparable to each other:

$$\Delta F_i^{\text{norm}} = \left(f_i(x_j^{\text{new}}) - f_i(x_j^{\text{old}}) \right) / f_i(x_j^{\text{new}})$$

for $i = 2, 3, ..., N_f.$ (15)

This relation can be summarized as:

$$\Delta F_i^{\text{norm}} = 1 - \frac{f_i(x_j^{\text{old}})}{f_i(x_j^{\text{new}})} \quad \text{for } i = 2, 3, ..., N_f.$$
(16)

However, this normalization technique is not adequately straightforward for the first objective function (ΔF_1) . Initially, the constraints in Eq. (14) must be normalized, which are divided by a constant, e.g. their initial value. After that, they are necessary to be similar with the objective function multiplied by its initial value:

$$F_1(x_j) = f_1(x_j) + b \sum_{k=1}^{N_c} \text{Max}$$
$$\left\{ 0, \left| \frac{f_1(x_j^{\text{initial}})}{g_k(x_j^{\text{initial}})} \right| g_k(x_j) \right\}.$$
(17)

In the next step, the differential of probability, assuming N_c constraints, can be written as:

$$\Delta F_1 = \Delta f_1(x_j) + b\Delta G(x_j), \tag{18}$$

where:

$$\Delta f_1(x_j) = f_1(x_j^{\text{new}}) - f_1(x_j^{\text{old}}),$$
(19)

$$\Delta G(x_j) = \sum_{k=1}^{N_c} \operatorname{Max} \left\{ 0, \left| \frac{f_1(x_j^{\text{initial}})}{g_k(x_j^{\text{initial}})} \right| g_k(x_j^{\text{new}}) \right\} - \sum_{k=1}^{N_c} \operatorname{Max} \left\{ 0, \left| \frac{f_1(x_j^{\text{initial}})}{g_k(x_j^{\text{initial}})} \right| g_k(x_j^{\text{old}}) \right\}.$$
(20)

Eq. (21) can be normalized if divided by $f_1(x_i^{\text{new}})$:

$$\Delta F_1^{\text{norm}} = 1 - \frac{f_1(x_j^{\text{old}})}{f_1(x_j^{\text{new}})} + \frac{b}{f_1(x_j^{\text{new}})} \Delta G(x_j).$$
(21)

Considering the proposed phases, this method can be named First Difference Last Normalizing (FNLD). In the contrary method, another way to achieve normalized function is to calculate the differential of probability after normalizing the objective function and constraints divided by their related value:

$$F_1^{\text{norm}}(x_j^{\text{old}}) = \frac{f_1(x_j^{\text{old}})}{f_j(x_j^{\text{new}})} + b \sum_{k=1}^{N_c} \text{Max} \left\{ 0, \frac{g_k(x_j^{\text{old}})}{|g_k(x_j^{\text{new}})|} \right\}, \quad (22)$$

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$$F_1^{\text{norm}}(x_j^{\text{new}}) = 1 + b \sum_{k=1}^{N_c} \text{Max}\left\{0, \frac{g_k(x_j^{\text{new}})}{|g_k(x_j^{\text{new}})|}\right\}, \quad (23)$$

$$\Delta F_1^{\text{norm}} = 1 - \frac{f_1(x_j^{\text{old}})}{f_1(x_j^{\text{new}})} + b\Delta G^{\text{norm}}(x_j), \qquad (24)$$

$$\Delta G^{\text{norm}}(x_j) = \sum_{k=1}^{N_c} \operatorname{Max} \left\{ 0, \frac{g_k(x_j^{\text{new}})}{|g_k(x_j^{\text{new}})|} \right\} - \sum_{k=1}^{N_c} \operatorname{Max} \left\{ 0, \frac{g_k(x_j^{\text{old}})}{|g_k(x_j^{\text{old}})|} \right\}.$$
(25)

In a similar way, the recent method can be named First Difference Last Normalizing (FNLD). In a case study, these methods were found to have higher performance in an HTSFCL design.

4. Performance of the proposed NPMOSA methods

To evaluate the proposed NPMOSA approaches in design of superconducting fault current limiter, a model in which an LSFCL is located at an outgoing feeder in a 20 kV distribution substation (single phase 11.5 kV) is considered. A simplified model is shown in Figure 4 as a single-phase equivalent circuit supposing a three-phase short-circuit fault occurring close to the substation.

A component in PSCAD/EMTDC environment was defined to achieve the combined electrical and thermal model of LSFCL. Simulations were carried out with a fault occurring at t = 100 ms (phase angle = 0) for maximum overshoot current and cleared after $\Delta t =$ 300 ms that is a suitable time for operating or re-closing the protection devices. The total simulation time was considered about 4 seconds, which is enough for quenching and restoration of the HTS. The simulation time interval of 10 μ s was sufficient to observe the transient pattern. The characteristics of the system and the selected LSFCL parameters are shown in Table 1.

In order to evaluate the operating characteristic and limiting behaviors of the LSFCL, case simulations based on the sample parameters were carried out with and without the limiter. The feeder peak current in pre-fault state is 390 A and exceeds 4.5 kA at the



Figure 4. A single-phase equivalent circuit of an electrical system with LSFCL.

Table 1. System parameters in the simulation model.

Provide a construction of the simulation model.					
Symbol	Quantity	Value			
E	Phase voltage (kV)	$20/\sqrt{3}$			
R_l	Resistance of the line (Ω)	1.5			
L_l	Inductance of the line (mH)	15			
f	System frequency (Hz)	50			
Load	Load power of the system (MW)	3			
T_c	Critical temperature for HTS (K)	90			
T_0	Temperature of LN_2 (K)	77			
I_{c0}	Critical current in $T = T_c$ (A)	720			
R_{sh}	Shunt resistance to HTS (Ω)	0.01			
C_p	Specific heat of HTS $(MJm^{-3}K^{-1})$	1			
P	Cooling power (MW)	1.6			
V_{SC}	Volume of HTS (m^3)	2×10^{-2}			
A_{SC}	Cross section of HTS (m^2)	6×10^{-4}			
h	Height of iron core (m)	1.5			
μ	Relative permeability of core	65			
r_c	Radius of iron core (m)	0.4			
r_s	Radius of HTS cylinder (m)	0.52			
r_p	Radius of copper coil (m)	0.7			
n	Turn ratio of transformer (N_1/N_2)	120			
P_{ac}	Maximum AC loss (W)	22.1			



first cycle, and 3.25 kA in steady state without any limiter device. By locating an acceptable (pre-optimum design) LSFCL in the system, the fault current is reduced to 2.1 kA in the flux flow mode and 1.15 kA in normal mode of HTS. The feasibility of the model for limiting the fault current in the study system is shown in Figure 5, while temperature and resistance variations are illustrated in Figure 6.

The current limiting impedance of LSFCL consists of a pure resistance of the HTS and a magnetizing reactance of the magnetic circuit. Based on Eqs. (1) to (5), the estimated stray inductance and calculated magnetizing inductance of the LSFCL are about 8.33 mH and 398 mH with corresponding reactances of 2.616 Ω and 125.3 Ω , respectively. The HTS resistance ($R_{\rm HTSFCL}$) exceeds 0.32 m Ω and 0.75 m Ω in flux flow and normal mode in the fault occasion. These resistances entail corresponding values on the primary side 4.6 Ω and 10.8 Ω , respectively. The variations of



Figure 6. The variations of superconducting temperature and resistance on the fault occasion.



Figure 7. The variation of total limiting impedance.



Figure 8. The current of resistive and inductive part.

the total impedance of the LSFCL $(X_m || R_{\text{HTSFCL}})$ are shown in Figure 7. It is shown that the maximum limiting impedance is about 10.43 Ω prior to the fault removal from the feeder.

The variations of the current through the magnetizing reactance of transformer and the HTS during fault state are compared in Figure 8. Considering the two limiting impedances with different natures, an oscillation of 90° phase difference occurs. It is noticeable that the HTS current, reflected on the primary side, is comparable with reactance current. It can be seen that the HTS element, which lingers in normal mode during restoring time and gives rise to the limiting impedance, remains in the line current path leading to voltage sag on the load side.

As stated before, in order to perform applicable optimization of inductive HTSFCL design, this paper proposes two methods (FNLD and FDLN) for normalizing multi-objective simulating annealing algorithm. The formulation of these methods is described by treating the problem as a multi-objective programming which runs the combined electrical and thermal model several times for different parameters. The problems are concerned with minimizing or maximizing each objective, simultaneously, while accounting for the constraints.

In this study, three objective functions are considered for design of an inductive HTSFCL. The first objective function is maximizing the current limiting factor (α) that is the ratio of the let-through shortcircuit current (i.e., the short circuit current in the absence of a limiting device) to the limited shortcircuit current within the first half cycle [33]. Another one is minimizing the voltage sag (ΔE) during postfault clearance that is an important index in power quality issue and finally, the last objective function is minimizing the HTSFCL cost (C) proportional to superconductor, copper, and iron volume. These objective functions are varied by electrical, thermal, and dimensional parameters being summarized in Table 1. As the majority of the parameters are usually invariable and dependent on one another or beyond the control of the designer, this study concentrates on superconductor and transformer parameters, i.e. HTS volume (V_{sc}) , transformer turn ratio (n), magnetic permeability (μ) , and core dimensions $(r_c \text{ and } h)$. Other design parameters have no considerable effect or can be written as selected variables. Hence, the problem can be formulated as:

$$f_1(x_1, x_2, x_3, x_4, x_5) = \alpha(V_{sc}, n, \mu, r_c, h),$$

$$f_2(x_1, x_2, x_3, x_4, x_5) = \Delta E(V_{sc}, n, \mu, r_c, h),$$

$$f_3(x_1, x_2, x_3, x_4, x_5) = C(V_{sc}, n, \mu, r_c, h).$$
(26)

Thus, the final optimization problem can be summarized as a standard minimization:

$$\operatorname{Min}\left\{\frac{1}{\alpha}, \Delta E, C\right\}.$$
(27)

The problem is completed by adding constrains to the above optimization functions. In the proposed approach, two constrains were assumed:

- (a) The limiting ratio (β) is the ratio of the peak limited current to peak nominal current, $\beta \geq 2$, allowing the relays and fault detection systems to start.
- The normal ac losses (P_{ac}) must not be greater (b) than 15 watts. These constrains can be formulized as:

$$g_1(x_1, x_2, x_3, x_4, x_5) = \beta(V_{sc}, n, \mu, r_c, h) \ge 2,$$

Parameter	Symbol	Min	Start	Max
1 arameter	Symbol	value	value	value
$HTS volume (m^3)$	V_{SC}	10-3	4×10^{-3}	10^{-2}
Turn ratio	n	80	120	150
$\operatorname{Permeability}$	μ	50	65	80
Core radius (m)	r_c	0.38	0.4	0.48
Core height (m)	h	1	1.5	2

Table 2. Limits of variables for optimization LSFCL.

Table 3.	Parameters	of	optimization	algorithm.
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Parameter	Symbol	Value
Maximum temperature for iteration	T_{\max}	5000
Initial temperature for objectives	T_{fi}	$10^{-4} - 10^4$
Decreasing factor	r	0.99
Importance of constrains	b	5

$$g_2(x_1, x_2, x_3, x_4, x_5) = P_{ac}(V_{sc}, n, \mu, r_c, h) \le 15.$$
(28)

Eventually, the problem can be defined as:

$$\operatorname{Min}\left\{\frac{1}{\alpha}, \Delta E, C\right\}$$

$$\Rightarrow \text{ Subject to}: \begin{cases} \frac{1}{\beta} - 0.5 \leq 0\\ P_{ac} - 15 \leq 0 \end{cases}$$
(29)

The limits of the selected variables and optimization parameters are shown in Tables 2 and 3, respectively. The mini and max values of variables were based on the stipulated and actual limiting performances [19].

In Table 3, dimensionless parameters, i.e. T_{max} , T_{fi} , r, and b, are maximum temperature of SA, initial temperature of any objective, decreasing factor of temperature, and penalty factor (describing the importance of constrains), respectively.

To achieve optimal design of LSFCL, several iterations in each simulation have been performed and for any set of objective priorities, the most appropriate design is recognized. As the objective functions are not in the same dimensions, normalized probabilities for any functions and constraints can be achieved by FDLN or FNLD methods. For example, when all objectives have the same importance by equal annealing temperature, the variations of parameters, objectives, and constraints are presented in Figures 9 to 11, respectively, for comparing FDLN and FNLD methods.

Considering nine priority levels and three objectives, Figure 12 shows 729 (i.e. 9^3) round optimization process results for the objective functions resulted by changing the temperature of the inter-proportional objectives based on priority selection, i.e. 10^4 (extremely



Figure 9. Comparison of design parameters in FDLN and FNLD for equal importance of objectives: (a) Turn ratio; (b) permeability; (c) core height; (d) core radius; and (e) HTS volume.

high), 10^3 (very high), 10^2 (high), 10^1 (slightly high), 10^0 (average), 10^1 (slightly low), 10^2 (low), 10^{-3} (very low), and 10^{-4} (extremely low), using FDLN technique. Figure 13 shows the related results for the FNLD method. In these figures, blue points are the final



Figure 10. Comparison of objectives results in FDLN and FNLD for equal importance of objectives: (a) Limiting factor; (b) voltage sag; and (c) cost.



Figure 11. Comparison of constraints results in FDLN and FNLD for equal importance of objectives: (a) Limiting ratio; and (b) AC loss.



Figure 12. Final Pareto front based on FDLN method.



Figure 13. Final Pareto front based on FNLD method.

points which are not dominated by one another and each of them can be a solution for the purpose design of an LSFCL in a distribution system through the priority of the objective functions. As the Pareto fronts (Figures 12 and 13) present difference in only few points, these two methods are acceptably capable of finding Pareto points and drawing Pareto front. Further required points can be obtained by increasing ranking scale of the criteria higher than nine.

5. Comparison of the two proposed methods

Considering numerical results from simulations, as the first observation, the FNLD method is proportionally faster than another. While the FNLD algorithm is carried out in about 432 seconds by a Pentium IV 3.40 GHz, the FDLN takes 574 seconds, i.e. about %30 slower. In FNLD method, the penalty factor of constrains (b) is more effective than that in FDLN. However, in a similar b, the FDNL produces enhanced cases. In both methods, increasing the importance of objectives (T_{fi}) results in better answers.

6. Conclusion

In this paper, a novel heuristic approach for optimization of limiting devices based on multi-objective criteria decision making was described via two methods. This was accomplished by modeling an inductive superconducting fault current limiter in PSCAD/EMTDC platform. A compromise between three optimal functions, i.e. current limiting factor, voltage sag, and economic construction cost, was extracted via nondominated solutions while considering two constraints, i.e. limiting ratio and ac loss. Both the proposed methods were compared and merits of each of them were discussed. However, the proposed methods were set for a particular case study; the designer can figure out exactly how much more cost is incurred on one objective depending on the priority of other objectives.

The heuristic optimization approach set out in this paper was for a particular case study. However, a designer must be capable of selecting any of the solutions lying on the Pareto front without jeopardizing optimality. More importantly, the designer can tell exactly how much more cost is incurred on a certain objective if another one is favored by a certain amount.

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